Notice of References Cited					09/696,955 Reexamina TAMURA I				
					Matthew A. Anderson 1765			Page 1 of 1	
				U.S. P	TENT DOCUMENTS				
*		Document Number Country Code-Number-Kind Code	Date MM-YYYY		Name Classif			Classification	
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7					itle Date, Publisher, Ed				
X	- U	Wolf et al., Silicon Processing for the VLSI Era, Volume 1: Process Technology, Lattice Press, Sunset Beach, CA, USA, pp. 8-, 2327, 32-33, 59, 529-532, 1986.							
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	w					<u>.</u>			
	х								
Dates J.S. Pa	in MM- Ienland	s reference is not being furnished with thi YYYY format are publication dates. Class I Trademark Office Rev. 01-2001)	sifications may be	US or foreig	707.05(a).) gn. References Cited	6/01/	/ ÚÝ Par	t of Paper No. 12	

Notic of Referenc s Cit d Application/Control No. O9/696,955 Page 1 of 1 U.S. PATENT DOCUMENTS Applicant(s)/Patent Under Reexamination TAMURA ET AL. Art Unit Page 1 of 1

				U.S. PATENT DOCUMENTS		
*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification	
X	Α	US-6284093 B1	09-2001	Ke et al.	156	345
1	В	US-6299982 B1	10-2001	Tamatsuka et al.	· 428	446
X	С	US-6190495 B1	02-2001	Kubota et al.	156	345
	D	US-				
	Æ	US-	/			
	F	US-				
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			F	OREIGN PATENT DOCUMENTS		
$\neg \neg$		Document Number	Date		7	

* Document Number Country Code-Number-Kind Code MM-YYYY Country Name Classification

O P Q R S S

NON-PATENT DOCUMENTS

			HONA ATENT DOCUMENTS					
*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)						
X	U	Wolf et al., Silicon Processing for the VLSI Era, Volume 1: rocess Technology, Lattice Press, Sunset Beach, CA, USA, pp. 8, 23-27, 32-33, 59, 1986.						
	٧							
	w							
	x							

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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